

Serial No. 09/801,637

Docket No. P-0202

Amdt. dated December 23, 2004

Reply to Office Action of September 30, 2004

Amendments to the Abstract:

Please replace the current Abstract with the attached Abstract.

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ABSTRACT OF THE DISCLOSURE

An interleaver memory access apparatus of a CDMA system including an interleaver memory for storing code symbols to be transmitted; a shift register unit for simultaneously receiving 18 bit code symbols from the interleaver memory and outputting the 18 bit code symbols in three sets of 6 bit code symbols; an index decoding unit for decoding the 6 bit code symbols outputted from the shift register unit and generating a Walsh bit index; an address generator and control logic for controlling an access operation of the interleaver memory and an input and output operation of the shift register unit and the index coding unit; and an orthogonal modulator for outputting 64 bit Walsh codes based on the Walsh bit index outputted from the index decoding unit.